

**RAJIV GANDHI UNIVERSITY
RONO HILLS:: DOIMUKH**

No. AC-1008/FDP/2011

Dated 10th April, 2018

ORDER

Sanction is hereby accorded to an amount not exceeding Rs 1,30,000/- (Rupee one lakh thirty thousand) only, to meet up fooding, lodging and other expenses in connection to conduct 6(six) days long Faculty Development programme on **Digital Signal Procession** in collaboration with Electronics and ICT,IIT, Guwahati, which is to be held from 30th April to 5th May 2018.

Approval is also hereby accorded to draw the advance amount of Rs 1,30,000/- (Rupee one lakh thirty thousand) only in favour Mr. Maibam Sanju Meetei, Assistant Professor Coordinator programme Department of Electronics & Communication, Rajiv Gandhi University subject to final adjustment within the month.

The expenditure shall be within sanctioned amount.

This issue with the approval of the Vice-Chancellor.

Head of a/c-01-ACD-01


Sd/-
Finance Officer

No AC-1008/FDP/2011/17

dated the 12th April 2018

Copy to:

- 1 Finance Officer for information.
- 2 Assistant Registrar (Bill/DDO) for information.
- 3 Head i/c Dept of of Electronics & Communication for information
- ✓ 4 Mr. Maibam Sanju Meetei, Assistant Professor Coordinator programme Department of Electronics & Communication for information.
- 5 Office Copy.


Section Officer (Academic)



राजीव गांधी विश्वविद्यालय

(केन्द्रीय विश्वविद्यालय)

रोनो हिल्स, पी. ओ. दोईमूख - ७९९९९२

(अरुणाचल प्रदेश)

RAJIV GANDHI UNIVERSITY

(A Central University)

Rono Hills, P.O. Doimukh - 791 112

(Arunachal Pradesh)

File No.AC-1008/FDP/2011

To,

Mr. Maibam Sanju Meetei,
Assistant Professor & Coordinator
Department of Electronics & Communication
Rajiv Gandhi University

Dated 16th April, 2018

Sub: - Regarding approval for conduct of FDP.

Ref: - Your letter No. Nil Dated 2nd April, 2018

Sir,

With reference to your letter cited above, I am delighted to inform you that the University Authority has approved your proposal for the conduct of a six days long Faculty Development Programme in Digital Signal Processing in collaboration with Electronics and ICT Academy, IIT- Guwahati, scheduled to be held from 30th April, to 5th May 2018 in the Department of Electronics & Communication and Dept. of Computer Science & Engineering, RGU.

This is for your kind information and necessary action.

Yours sincerely

(Dr. David Pertin)
Joint Registrar (Acad./Cont.)

Dated 17th April 2018

File No AC-1008/FDP/2011 / 3C

Copy to -

1. Dean, Faculty of Basics Science, Information Technology & Engineering for information.
2. Head, Dept of Computer Science & Engineering for information
3. Head i/c. Department of Electronics & Communication for information
4. Head, E & ICT Academy, IIT-Guwahati, for information
5. Office copy

Joint Registrar (Acad./Cont.)

Tel: +91-360-2277252 (Off), Fax: +91-360-2277317

Faculty Development Programme

A Faculty Development Programme on "DIGITAL SIGNAL PROCESSING" was held by Dept. of Electronics and Communication and Dept. of Computer Science and Engineering, Rajiv Gandhi University, Rono Hills Doimukh for 6 day i.e. from 30th April to 05th May, 2018, Venue at IDE Conference Hall, Rajiv Gandhi University in collaboration with E & ICT Academy, IIT Guwahati and STEPS Knowledge Services Pvt. Ltd.

The Objective of the Course is "This Faculty Development Programme is designed to meet the need of the engineering faculty to get a deep exposé of Digital Signal Processing which can be very well extrapolated to any application domain."

The Expert from Industry:

▪ **N. Kavitha**, Director, STEPS Knowledge Services Pvt. Ltd.

Holds a Technical Director of STEPS Knowledge Services Pvt Ltd, India and handling Design and Development of Special Purpose Machines and Embedded System Development, IP related activities and Heading the Training Division catering both to Corporate and Academia on emerging technologies. *An Electronics & Communication Engineer with more than 30 years of industrial experience focused on design and development including fundamental and applied research.* Holds MS in Software Systems from BITS, Pilani, MBA in Finance, PG Diploma in IPT from NLSIU, Bangalore and DOM from IGNOU. *Speaker in various International and National conferences on topics related to Embedded Systems. Has worked extensively with Design Houses, Consultants, OEMs, Collaborators in Germany, Italy, France, Switzerland, UK, USA, Japan and Malaysia, whereby gained the experience of converting ideas to products and meeting international work practices and standards. Co-inventor in various patent applications relating to specific industrial engineering applications.* A passionate trainer to impart latest and emerging technology to the stakeholders who are in need of the same.



▪ **M. Srinivasan**, Assistant Engineer, STEPS Knowledge Services Pvt. Ltd.

Experience in Design and development of Embedded system with MicroChip and TI Working with DSP and Matlab, working with IOT and application, TIRTOS, TINA TI and WEBENCH for simulation and testing, Robotics-TIRSLK and Development of RFID tag



Course Outcome: The participants will be given a thorough understanding of the following topics

- Overview on Digital Signal Processing.
- Architecture and applications of C2000 based 32 bit Fixed Point DSP Engine from Texas Instruments.
- The peripherals including I/O Ports, Universal Serial Interfaces, ADC, Timers, Enhanced PWM (ePWM) Modules, On Chip Flash Access are explained and worked upon. Enhanced architectures of Floating Point Unit, Control Law Accelerator for parallel processing are also explained.
- Working with TI-RTOS to meet real time signal processing and control application requirements

The Committee Members are:

Chairman

• **Prof. Utpal Bhattacharjee**, *Dean, Faculty of Basic Science, Information Technology & Engineering Technology*

Co-ordinators

• **Mr. Maibam Sanju Meetei**, *Asst. Prof., ECE*

Co-coordinators

• **Mr. Ani Taggu**, *Head & Associate Prof., CSE*

• **Mr. Jagdeep Rahul**, *Head & Asst. Prof., ECE*

• **Mr Bhaskar Jyoti Chutia**, *Asst. Prof., CSE*

Members

▪ **Ms. Champa Tanga**, *Asst. Prof. ECE*

▪ **Mr. Kurmendra**, *Asst. Prof., ECE*

▪ **Dr. Marpe Sora**, *Asst. Prof., CSE*

▪ **Mr. Firos A**, *Asst. Prof., CSE*

▪ **Ms. Bomken Kamdak Bam**, *Asst. Prof., CSE*

▪ **Mr. Sikdar Md. Sultan Askari**, *Asst. Prof., CSE*

▪ **Mr. Satish Kumar Das**, *Asst. Prof., CSE*

▪ **Ms. Gitanjali Choudhury**, *Asst. Prof., CSE*

▪ **Ms. Ankita Chakraborty**, *Asst. Prof., CSE*

▪ **Ms. Jowa Yangchin**, *Asst. Prof., CSE*

▪ **Ms. Aditya R. Pillai**, *Asst. Prof., CSE*

The Course Coordinators from Academy:

Prof. Ratnajit Bhattacharjee, *Principal Investigator, E&ICT Academy, IIT Guwahati.*

Dr. Gaurav Trivedi, *Co-Principal Investigator, E&ICT Academy, IIT Guwahati*

The 6 (six) day faculty development programme was inaugurated by felicitation the guests and lighting of lamp by the Honorable Chief Guest Prof. Amitava Mitra, Finance Officer, Rajiv Gandhi University, and Honorable Guest of Honor Prof. Tomo Riba, Registrar (I/C), followed by University Song from the student of Music Department, Rajiv Gandhi University. The programme was finish after the completion of all technical session followed by Valedictory session hosted by Prof. Utpal Bhattacharjee, Dean, Faculty of Basic Sciences, Information Technology and Engineering Technology . The programme was benefited by 27 (twenty seven) candidates.

All the participants are given a course completion certificate and with a grade card for their performance base on completion of project and MCQ take on the last day of the programme.

Enclosed:

1. Photocopy of all grade card,
2. Boucher,
3. Sanction order and
4. Some Random photo taken during the programme.