RGUCET 2022

M Tech in ECE

1	Which country will host the FIFA World Cup 2022 tournament?								
	a) France	b) Qatar		c) Br	azil		d) Japan	В	Qatar
2	Which country	will host the	Intern	ation	al Crick	ket	Council (ICC)		
	World Cup 202	23?		\ T				0	T 1'
	a)England	b) Australia	a	c) In	dia		d) South	C	India
3	In which year	India launched	Look	Fast	Policy?	,	Amca		
5	a) 1986	b) 1992	LOOK	$\frac{2030}{(1)}$	10110y. 196		d) 1994	R	1992
4	The capital city	v of Ukraine		c) 17	70		u) 1991	D	1772
	a) Kviv	b)Kharkiy		c) O(lessa		d) Dninro	Δ	Kviv
5	Which is the la	rgest state in No	orth F	er I	ndia?		d) Dilipio	Λ	Kylv
	a) A rupachal	h) Assam		$\frac{ast, 1}{c}$	agaland		d) Tripura	۸	Arupachal
	Pradesh	0) Assain		C) IN	agaianu		u) Ilipula	А	Pradesh
6	Which of the fo	ollowing is NO	Га Cr	vnto	Curren	cv^{9}			
	a)Bitcoin	b)Ethereun		$\frac{c}{c}$	ether	<u> </u>	d)Doger	D	Doger
7	Which country	is NOT connec	ted wi	$\frac{c}{ith A}$	runacha	al P	radesh?	D	Doger
,	a)China	b) Myanma	ar	$\frac{1}{c}$ Bł	nutan		d)Bangladesh	D	Bangladesh
8	Where is Leng	nui Airport?	*1	c) DI	Iutuii		d)Duligiudesii	D	Dunghucom
0	a) Arunachal	b) Miz	oram	C) Assam	1	d) Nagaland	B	Mizoram
	Pradesh	0) 1112	orani		<i>)</i> 1 135 d 11	1	u) Maganana	D	WIIZOram
9	Where the Shir	ui Lily grows?							
	a) Tripura	b) Mizoran	n	c) M	anipur		d) Nagaland	В	Manipur
10	Which of the fo	ollowing countr	y is no	ot a n	nember	of (Quadrilateral		
	Security Dialog	gue (QSD) or Q	UAD	?					
	a) India	b)Australia	L	c) Fr	ance	d)	United State	С	France
11	Select the wron	ngly spelt word				1			
	a) expire	b) explicit	c) e	explo	de	d)0	exploite	D	exploite
12	In each of the f	following questi	ons, c	choos	e the co	orrec	ctly spelt		
	word.	b) Douguat		Dog	not	4)1	Pouquatta	D	Douguat
12	a)Bouquete	D) Douquet) воч	uet	u)	Douquette	D	Douquet
15		ges public prop		-		-1)	X 7 1 - 1	D	V
	a) Cynosure	Demagogue	C) f	Epicu	ire	a)	vandal	D	vandal
14	My sister's mar	riage passed	p	eacef	fully.	1			
	a) away	b) by	c) c	off		d)	out	С	off
15	Direct and Indi	rect Speech : A	nil sai	id, "A	li desei	rvec	l the prize."		
	a) Anil says	b) Anil said	c) A	Anil s	said	d)	Anil said that	D	Anil said that

	that Ali	that Ali	that Ali has	Ali had deserved		Ali had
	deserves the	deserves the	deserved the	the prize.		deserved the
	prize.	pirze.	prize.			prize.
16	Tsunamis are N	NOT caused by	F			F
	a) Hurricanes	b)Earthquakes	c)Earthquakes	d) Volcanic	Α	Hurricanes
		-,	.,	eruptions		
17	The hottest pla	net in the solar sy	ystem?			
	a)Mercury	b)Venus	c)Mars	d)Jupiter	В	Venus
18	Guwahati High	n Court is the judi	icature of			
	a)Assam	b)Nagaland	c)Arunachal	d)All of the	D	All of the
			Pradesh	above		above
19	Friction can be	reduced by chan	ging from			
	a)rolling to	b)sliding to	c)dynamic to	d)potential	В	sliding to
	sliding	rolling	static	energy to kinetic		rolling
				energy		
20	Garampani san	ctuary is located	at			
	a)Gangtok,	b)Kohima,	c)Diphu,	d)Junagarh,	C	Diphu, Assam
	Sikkim	Nagaland	Assam	Gujarat		
21	Today is Varur	n's birthday. One	year from today	he will be twice		
	as old as he wa	is 12 years ago. H	Iow old is Varun	today?		
	a)20 years	b)21 years	c)22 years	d)25 years	D	25
22	Joule is relat	ed to Energy ir	n the same way	as Pascal is		
	related to	?		I		_
	a)Purity	b)Density	c)Pressure	d)Volume	C	Pressure
23	Museum is re	elated to Curate	or in the same	way		
	as Prison is r	elated to	_?			
	a)Warden	b)Jailor	c)Monitor	d)Manager	B	Jailor
24	A farmer built	a fence around h	is square plot. He	e used 27 fence		
	poles on each s	side of the square	. How many pole	es did he need		
	altogether?				_	10.1
	a)100	b)101	c)102	d)104	D	104
25	A pineapple co	sts Rs. 7 each. A	watermelon cos	ts Rs. 5 each. X		
	spends Rs. 38 o	on these fruits. The	he number of pin	eapples purchased		
	18					
	a)2	b)3	c)4 d)Da	ita inadequate	C	4
26	The following	hexadecimal num	the $(1E.43)_{16}$ is	equivalent to		
	a) (36.506) ₈	b) (36.206) ₈	c) $(35.506)_8$	d) (35.206) ₈	b)	(36.206) ₈
27	How many bits	are needed to sto	ore one BCD dig	it?	1	
20	a)l	b)2	c)3	d)4	d)	4
28	In the toggle m	ode, a JK flip-flo	p has		1 \	T 1 T7 4
	a) $J = 0, K = 1$	b) J = 1, K = 1	c) J = 0, K = 0	$J \mid d) J = 1, K = 0$	b)	J = 1, K = 1
29	A full adder ca	n be constructed	trom		``	
	a) 2 Full	b) 2 Full	(c) 2 Half	a) 2 Full adder	c)	2 Half adder

			-			
	adders	adders and 1	adder and 1	and 1 XOR		and 1 OR
		AND Gate	OR Gate	Gate		Gate
30	In a counter, ma	ximum number	of states possible	with 4 flip flops		
	is					
	a)15	b)16	c)3	d)4	b)	16
31	A 32:1 Multiple	xer will have ho	w many select lin	nes?		
	a) 3	b)4	c)5	d)32	c)	5
32	Which of the fol	lowing is not a d	combinational cir	rcuit?	Í	
	a) Flip Flop	b) Shift	c) Counter	d)All of above	d)	All of above
		Register	-,	.,	- /	
33	The interface be	tween an analog	signal and a dig	ital processor is		
	a) D/A	b) A/D	c) Modulator	d) Demodulator	b)	A/D converter
	converter	converter	-,	.,	- /	
34	The speech signa	al is obtained af	ter	I		
	a) Analog to	b) Digital to	c) Modulation	d) Ouantization	b)	Digital to
	digital	analog	•) 1:10 001001011		0)	analog
	conversion	conversion				conversion
35	Telegraph signal	s are examples	of			Conversion
	a) Digital	b) Analog	c) Impulse	d) Pulse train	(a	Digital signals
	signals	signals	signals	u) i uise tium	(u	Digital bightilb
36	DTFT is the repu	resentation of	Signais			
	a) Periodic	b) Aperiodic	c)Aperiodic	d) Periodic	b)	Aperiodic
	Discrete time	Discrete time	continuous	continuous	0)	Discrete time
	signals	signals	signals	signals		signals
27	Signals	signals	signals	signals		signals
57	1) Ite ob	cility to dotormir	a the frequency	component of the		
	1) Its au		le the frequency	component of the		
	2) Dama	l aval of poiss				
	$2) \text{Kellin} \\ 2) \text{Eilter}$					
	3) Filler	tization of sign	.1			
	$\frac{4}{2}$ Quan	h) 1 and 2 and	ll	d) All the four		1 and 2 and
	a) 1, \angle and \Im	b) I and 2 are	c) I and 5 are	u) All the lour		1 and 5 are
20	The resident f		$(1 + 2\pi + -2)^{-2}$	are correct		correct
38	The region of co	h)1	$(1+2X+X^{-})$ 18	d) Do -:+:	1- \	1
20			c) Negative	a) Positive	D)	1
39	For a system fun	H(s) to be	stable	1. 771 1 11	、 、	
	a) The zeros	b) The zeros	c) The poles	d) The poles lie	c)	The poles lie
	lie in left half	lie in right	lie in left half	in right half of		in left half of
	of the s plane	half of the s	of the s plane	the s plane		the s plane
		plane				
40	The factors that	cause quantizing	g error in delta m	odulation are		
	a) Slope	b) Granular	c) White	d) Both a and b	d)	Both a and b
	overload	noise	noise	are correct		are correct
	distortion					
41	A semiconducto	r is formed by	bonds.			
	a) Covalent	b)	c) Co-	d) None of the	a)	Covalent

-						
		Electrovalent	ordinate	above		
42	Which among th	e following is n	ot an advantage	of an open loop		
	system?					
	a) Simplicity	b) Easy	c) Rare	d) Requirement	d)	Requirement
	in construction	maintenance	problems of	of system		of system
	& design		stability	recalibration		recalibration
				from time to		from time to
				time		time
43	Which among th	e following repr	resents an illustra	ation of closed		
	loop system?					
	a) Automatic	b) Automatic	c) Bread	d) Electric hand	b)	Automatic
	washing	electric iron	toaster	drier		electric iron
	machine					
44	The output is sai	d to be zero stat	e response becau	ise		
	condition	ns are made equa	al to zero.			
	a)Initial	b)Final	c)Steady state	d)Impulse	a)	Initial
				response		
45	Basically, poles	of transfer funct	ion are the Lapla	ace transform		
	variable values v	which causes the	transfer function	n to become		
	·					
	a)Zero	b)Unity	c)Infinite	d)Average	c)	Infinite
		-		value		
46	By equating the	denominator of	transfer function	to zero, which		
	among the follow	wing will be obt	ained?			
	a)Poles	b)Zeros	c)Both a and b	d)None of the	a)	Poles
				above		
47	The output signa	al is feed back at	the input side fr	rom the		
	point	•				
	a)Summing	b)Differential	c)Take-off	d)All of the	c)	Take-off
				above		
48	In a parallel com	bination, the dir	rection of flow of	f signals through		
	blocks in paralle	l must resemble	to the main			
	a)Forward	b)Feedback	c)Opposite	d)Diagonal	a)	Forward
49	The steady-state	error of a feedb	ack control syste	em with an		
	acceleration input	ut becomes finite	e in a			
	a)type 0	b)type 1	c)type 2	d) a and b	c)	type 2 system
	system	system	system			
50	For a type one sy	ystem, the stead	y – state error du	e to step input is		
	equal to	•				
	a)infinite.	b)zero.	c)0.25.	d)0.5	b)	zero.
51	A capacitor is ch	arged by a cons	tant current of 2	mA and results in		
	a voltage increas	e of 12 V in a 1	0 sec interval. Th	ne value of		
	capacitance is					
	a) 0.75 mF b) 1.33 mF	c) 0.6 mF c	l) 1.67 mF	d	1.67 mF
52	A branch has 6 n	ode and 9 branc	h. The independ	ent loops are	1	

	1	1		1		r
	a)3	b)4	c)5	d)6	В	4
53	A silicon pn ju					
	$=10^{17} \text{ cm}^{-3}$.					
	The built-in ve	oltage is				
	a) 0.63 V	b) 0.93 V	c) 0.026 V	d) 0.038 V	В	0.93 V
54	In a uniformly	doped GaAs junc	tion at $T = 300$) K, at zero bias,		
	Only 20% of the built in point of the built	ne total space charged	ge region is to $7 - 1.20$ V. The	be in the p-region.		
	concentration	in n-region is	- 1.20 V. IIK	e majority carrier		
	a) 1 * 10 ¹⁶	b) $1*10^{22}$ cm ⁻³	c) $4 * 1016$	d) $4 * 10^{22}$ cm ⁻³	С	$4 * 10^{16} \text{ cm}^{-10}$
	a) 1 10 am-3		c) = 10			3
	CIII		CIII ²			
55	An abrupt silie	con <i>pn</i> junction at :	zero bias and Z	T = 300 K has		
	dopant concer	ntration of $N_a = 10$	17 cm^{-3} and Λ	$l_d = 5* 10^{15} \text{ cm}^{-3}$.		
	The Fermi lev	rel on n - side is		u		
	a) 0.1 eV	b) 0.2 eV	c) 0.3 eV	d) 0.4 eV	С	0.3 eV
56	An pn junction	n diode is operating	g in reverse bi	as region. The		
	applied revers	erse current reaches				
	90% of its rev	erse saturation cur	rent, is			7 0 (1 1
	a) 59.6 mV	b) 4.8 mV	c) 2.7 mV	d) 42.3 mV	A	59.6 mV
57	For an <i>n</i> -chan	nel MOSFET biase	ed in the satura	ation region, the		
	parameters are	$e K_n = 0.5 \text{ mA V}^2,$	$V_{TN} = 0.8$ V an	d l=0.01V ⁻¹ , and I_{DQ}		
	=0.75mA.The	value of g_m and r_d	o are			
	a) 0.68 mS,	b) 1.22 mS, 133	c) 1.22 mS,	d) 0.68 mS, 133	C	1.22 mS, 603
58	603 KW	KW	603 kW	KW		KW
50	For the circuit			sistance is		
		Ц	_			
			$\int 4 k\Omega$			
			- <u></u>			
		-	$\sum_{n=1}^{2 \text{ k}\Omega} \sum_{n=1}^{2 $			
			Ţ			
	a) 38 kW	b) 17 kW	c) 25	d) 47 kW	В	17 kW
			kW			
59	A Mealy syste	em produces a 1 ou	tput if the input	ut has been 0 for at		
	least two cons	ecutive clocks foll	owed immedia	uery by two or more		
	a)4	b)5	c)8	d)9	А	4
60	The diode log	ic circuit of fig. is	a			
					1	1

	V ₁	•D2				
	a) AND	b) NAND	c) OR	d) NOR	с	OR
61	In an 8085 mid executed while register B. As	croprocessor, the i e the contents of a a result carry flag	nstruction CM accumulator is and zero flag	P B has been less than that of will be respectively		
	a) set, reset	b) reset, reset	c) reset, set	d) set, set	D	set, set
62	The Laplace to	ransform of a func	tion f (t) is			
	a) $\int_0^\infty f(t) e^{-st}$	b) $\int_0^\infty f(t)e^{st}$	c) $\int_{-\infty}^{0} f(t) e^{st}$	$\int_{-\infty}^{0} f(t) e^{-st}$	А	$\int_0^\infty f(t) e^{-st}$
63	When is curre	nt in phase with th	e voltage?			
	a)When XL>XC	b)When XL <xc< td=""><td>c)When XL=XC</td><td>d)When XC=infinity</td><td>с</td><td>When XL=XC</td></xc<>	c)When XL=XC	d)When XC=infinity	с	When XL=XC
64	State space an	alysis is applicabl	le even if the i	nitial conditions are		
	a) Zero	b) Non-zero	c) Equal	d) Not equal	b	Non-zero
65	Conventional	control theory is a	pplicable to	systems		
	a) SISO	b) MIMO	c) Time varying	d) Non-linear	a	SISO
66	For an LTI d impulse respon	iscrete system to nse should be	be stable, the	square sum of the		
	a) Integral multiple of 2pi	b) Infinity	c) Finite	d) Zero	с	Finite
67	What is the du	ration of the unit s	sample respons	se of a digital filter?		
	a) Finite	b) Infinite	c) Impulse (very small)	d) Zero	b	Infinite
68	If $h_{lp}(n)$ deno frequency resp the high pass f	tes the impulse re- conse $H_{lp}(\omega)$, then filter in terms of H	esponse of a what is the from $\frac{1}{1p}(\omega)$?	low pass filter with equency response of		
	a) $H_{lp}(\omega - \pi/2)$	b) $H_{lp}(\omega + \pi/2)$	c) $H_{lp}(\omega-\pi)$	d) $H_{lp}(\omega + \pi)$	с	$H_{lp}(\omega-\pi)$
69	CMOS techno	ology is used in dev	veloping			

	a)	b))	c) digital	d) all of the	D	all of the
	microproces	microcontroller	logic	mentioned		mentioned
	sors	S	circuits			
70	P-well is creat	·				
	a) p subtrate	b) n substrate	c) p & n	d) none of the	В	n
			substrate	mentioned		substrate
71	Photoresist lay	yer is formed using	5			
	a) high	b) light	c)	d) silicon di oxide	b	light
	sensitive	sensitive	polysilicon			sensitive
	polymer	polymer				polymer
72	When the sour	rce of light is not s	un light then tl	he photo voltaic cell		
	is used as					
	a) Photo	b) Photo voltaic	c) Photo	d) Photo	С	Photo
	diode	cell	detector	transmitter		detector
73	The region wh	here the electrons a	nd holes diffu	sed across the		
	junction is cal	led	I			
	a) Depletion	b) Depletion	c)	d) Depletion	В	Depletion
	Junction	region	Depletion	boundary		region
			space			
74	Solar cells are	made from bulk n	naterials that a	re cut into wafer of		
	th	ickness.			_	
	a) 120-	b) 120-220µm	c) 180-	d) 180-240µm	D	180-240µm
	180µm		220µm			
75	Choose the co	prrect statement(s)				
	1) The gate cir	cuit impedance of	MOSFET 1s h	igher than that of a		
		··· 1 (
	11) The gate ci BJT	rcuit impedance of	MOSFET 18 I	ower than that of a		
	iii) The MOSI	FET has higher swi	itching losses	than that of a BJT		
	iv) The MOSI	FET has lower swit	tching losses th	han that of a BJT		
	a) Both i& ii	b) Both ii & iv	c) Both i&	d) Only ii	С	Both i& iv
			iv	•		



	a) 3.5 mF	b) 2.4 mF	c) 1.2 mF	d)) 2.6 mF	С	1.2 mF
77	In the circui	t of the fig. below t	the value of the v	voltage source E is		
	V_{2}	0.17				
	°×.	× 0 ^{0 V}				
		× 1V				
	, Ì					
	4 V 🗙 🗙	× 5 V				
	vo	6 10 V				
	r_1	b) -6V	c) 4V	d) 16V	Δ	-16V
78	Two coils of	f 2H inductorice of	c) + v	cories and are also	Π	-10 V
/0	nagnetically	r coupled to each	other the coeff	ficient of coupling		
	being 0.1.Th	e total inductance of	of the combination	on can be		
	a) 2 H	b) 2. 3H	c) 4H	d) 4.4H	D	4.4H
79	The minim	um number of 2	-input NAND	gates required to		
	implement a	2-input XOR gate	is	0 1		
	a) 4	b) 5	c) 6	d) 7	А	4
80	The number	of comparators re	equired in a 3-b	it comparator type		
	ADC is			1		
	a) 2	b)3	c)7	d)8	С	7
81	The number	of comparators in 4	4-bit flash ADC	is		
	a) 4	b) 5	c)15	d) 16	С	15
82	The resolution	on of a 4-bit counti	ng ADC is 0.5 V	olts. For an analog		
	input of 6.6	Volts, the digital ou	tput of the ADC	will be		
	a)1011	b)1101	c)1100	d)1110	D	1110
83	The output	Y of a 2-bit compa	rator is logic 1	whenever the 2-bit		
	input A is	greater than the	2-bit input B	. The number of		
	combination	s for which the out	put is logic 1, is a^{9}	<i>d</i>) 10	C	6
0.4	a) 4 W/h : - 1-	0,0		$\begin{array}{c} \mathbf{u} 10 \\ \mathbf{u} \mathbf{f} \\ \mathbf{u} \mathbf{u} \mathbf{u} \\ \mathbf$	C	U
84	which one of dielectric (Si	on the following proposed of MOSEET $_{0}$?	cesses is preferr	ted to from the gate		
		b) Molecular	c) Wet	d) Dry Oxidation	D	Dry
	Sputtering	Beam Epitaxy	Oxidation	d) Dry Oxidation		Oxidation
85	In MOSFET	fabrication. the cl	hannel length is	defined during the		
	process of	···· · · · · · · · · · · · · ·	0 10			
	a) Isolation	b) Channel Stop	c) Polysilicon	d) Lithography	С	Polysilicon
	Oxide	implantation	gate	step leading to		gate
	Growth		patterning	the contact pads		patterning
86	The Ebers-M	Ioll model of a BJT	is valid	1		
	a)only in	b) only in active	c) only in	d)in active,	D	in active,
	active	and saturation	active and	saturation and		saturation and

	mode	modes	cut-off modes	cut-off modes.		cut-off
						modes.
87	An increase	in the base recomb	ination of a BJT	will increase		
	a) the	b) the	c)the unity	d)the trans-	В	b) the
	common	breakdown	gain cut off	conductance g _m		breakdown
	emitter dc	voltage BV _{ceo}	frequency f _T			voltage BV _{ceo}
	current					
	gain β					
88	In an 8085 n	nicroprocessor, the	shift registers w	hich store the result		
	of an additio	n and the overflow	bit are, respectiv	vely.	-	
	a) A & B	b) A & F	c) C &A	d) B & F	В	A & F
89	In an 808	5 microprocessor	, which one	of the following		
	instructions	changes the conten	t of the accumula	ator?		
	a) MOV B,	b) PCHL	c) RNZ	d) SBI BEH	D	SBI BEH
	M					
90	An instructi	on used to set the	e carry flag in a	a computer can be		
	classified as				~	
	a) Data	b) arithmetic	c) logical	d) program	C	logical
0.1	Transfer			control		
91	The total nu	mber of memory	accesses involve	d (inclusive of the		
	op-code fetc	ch) when an 8085	processor execu	ites, the instruction		
	LDA 2003 19	5	\ 2	1) 4	D	
	a) 1	b) 2	c) 3	d) 4	D	4
92	In a microp	rocessor, the regis	ter which holds	the address of the		
	next instruct	ion to be fetched is			D	D
	a)	b) Program	c) Stack	d) Instruction	в	Program
	Accumulat	Counter	Pointer	Register		Counter
02	Or Which of th	fallowing angle	a madulation a	hanse nagyinag the		
95	which of the	ne following analo	g modulation so	and handwidth?		
	a) VSB	b) DSB SC			C	SSB
0.4					C	220
94	An AM sign	al is detected usin	g an envelope d	1 MUL and 2 KUL		
	requency an	An enpropriate A	al frequency are	I MHZ and 2 KHZ		
	respectively.	An appropriate V	alue for the th	he constant of the		
	a)500usee	b) 20usee	a) 10usaa	d)50usee	D	200000
05	The trian	D) 20µsec	c) topsec	ation of time 1-	Ъ	204860
95	not have the	neuric Fourier serie	es of an even fui	iction of time does		
	a) DC torm	b) Cogina Tama	a) Sina Tarm	d) odd harmonia		Sina tarm
	a) DC term	b) Cosine Term	c) sine renn	torm	C	Sine term
06	The Fourier	Series of an odd no	riodic function	contains only	C	
90		b) Cosin - Tom			C	Circo Tours
	a) Even	b) Cosine Term	c) Sine Term	a)Oud Harmonic	C	Sine Term
07	To obtain	any high input and		noon in a faadhaala		
フノ	IU UUtalli V	erv men mout alle	i outout IIIIDeua	nees in a recuback	1	1

	Amplifier, th					
	a) Voltage	b) Current	c) Voltage	d) Current Shunt	В	Current
	Series	Series	Shunt			Series
98	Crossover di	stortion behavior is	s characteristic of	f		
	a) class A	b) class B	c) class AB	d) common base	В	class B output
	output	output stage	output stage	output stage		stage
	stage					
99	A class-A					
	deliver a pov	ver rating of the tra	insistor should no	ot be less than.		
	a) 5W	b) 10W	c) 20 W	d) 40W	В	10 W
100	A network h					
	branches in t	he network is				
	a)13	b)12	c)11	d)10	С	11